Programme Regulations: 2023/24

Programme Titles:

Title Degree of Bachelor of Engineering with Honours in Electrical and Electronic Engineering International - Code: 1826U

Notes

(i) These programme regulations should be read in conjunction with the University's Taught Programme Regulations.

(ii) All optional modules are offered subject to the constraints of the timetable and to any restrictions on the number of students who may be taught on a particular module. Not all modules may be offered in all years and they are listed subject to availability.

(iii) *A compulsory module is a module which a student is required to study.*

(iv) All modules are delivered in Linear mode unless stated otherwise as Block, eLearning or distance learning.

(v) If a candidate meets the requirements for the four year degree, MEng with Honours in Electrical and Electronic Engineering (H605) they may transfer to that programme at any time before the start of Stage 3.

(vi) *Programme transfers for Tier 4 students may be restricted by current Tier 4 rules. Please refer to the Visa Team for advice.*

1. Stage 1

(a) All candidates shall take the following modules, all of which are compulsory:

Code	Descriptive title	Total Credits	Credits Sem 1	Credits Sem 2	Level
INU1130	English for Academic Purposes for International Engineers	20	10	10	4
INU1121	Engineering Mathematics for International Year One Engineering	20	10	10	4
INU1122	Sustainable Design, Creativity and Professionalism for International Year One Engineering	30	10	20	4
INU1123	Electrical and Magnetic Systems for International Year One Engineering	15	10	5	4
INU1124	Electronics and Sensors for International Year One Engineering	10	0	10	4

INU1125	Thermofluid Mechanics for International Year One Engineering	15	5	10	4
INU1126	Engineering Materials for International Year One Engineering	15	15	0	4
INU1127	Mechanics for International Year One Engineering	15	5	10	4

(b) **Re-sit assessment**

For the English for Academic Purposes module, the following will apply:

The required pass mark for the module is 60. The required competence level (as determined by UKVI regulations) in each of the four subskills (reading, listening, writing and speaking) is 55. A student will be granted one resit opportunity in either of the following cases:

If a student fails to achieve the required pass mark (60) for the module. In this case, the student will only be required to resit those subskills where they have failed to achieve a subskill pass mark of 60. The result achieved in the resit for the subskill in this case will be uncapped, but the overall module result following the resit will be capped at the pass mark of 60.

If a student has achieved a module mark of 60 or more but has one or more subskill mark of less than 55, then in line with Programme Regulations the student has not passed the module. In this case, the student will be required to resit those subskills where they have failed to achieve the competence level of 55. The result achieved will be uncapped but the overall module result following the resit will be capped at the pass mark of 60.

In both cases, marks of over 60 achieved in any subskill at a first attempt will need to be capped to ensure overall module marks following either resit case do not exceed 60. University selectors may be provided with uncapped marks so that a decision can be made based on the student's actual English language competence level.

2. Stage 2

(a) All candidates shall take the following compulsory modules:

Code	*Descriptive title*	*Total Credits*	*Credits Sem 1*	*Credits Sem 2*	*Level*
EEE2008	Project and Professional Issues	20		20	5
EEE2009	Signals and Communications	20	20		5
EEE2014	Semiconductor Devices and Analogue Electronics	20	20		5
EEE2021	Computer Programming and Organisation	20	10	10	5
ENG2026	Automatic Control Systems	10		10	5
ENG2025	Digital Electronics	10		10	5
ENG2029	AC Electrical Power and Conversion	10		10	5
ENG2031	Mathematical Modelling and Statistical Methods for Engineering	10		10	5

3. Stage 3

(a) All candidates shall take the following compulsory modules:

Code	*Descriptive title*	*Total Credits*	*Credits Sem 1*	*Credits Sem 2*	*Level*
ENG2032	Business and Law for Engineers	10	5	5	5
EEE3094	Individual Project and Technical Report	30	10	20	6

(c) All candidates shall take optional modules normally selected from the following list so that the total number of credits is 120:

Code	*Descriptive title*	*Total Credits*	*Credits Sem 1*	*Credits Sem 2*	*Level*
EEE3023	Digital Communication Systems	20	10	10	6
EEE3024	Industrial Automation & Control Systems	20	10	10	6
EEE3025	Power Electronics – Design & Application	20	10	10	6
EEE3026	Electronic Devices and Semiconductor Technology	20	10	10	6
EEE3027	Integrated Circuit Design and Embedded Systems	20	10	10	6
EEE3028	Electrical Machines and Drives	20	10	10	6
EEE3029	Net-Zero Networks	20	10	10	6
EEE3030	Signal Processing and Machine Learning	20	10	10	6

With the approval of the Degree Programme Director alternative optional modules to those listed above may be selected.

4. Assessment Methods

Details of the assessment pattern in each module are explained in the module outline.

5. Degree classification

Candidates will be assessed for degree classification on the basis of all the modules taken at Stages 2 and 3 with the weighting of the stages being 1:3 for Stage 2 and Stage 3 respectively.