Programme Regulations: 2025/26

Programme Titles:

Degree of Master of Science in Embedded Systems and Internet of Things -Code: 5134F

Degree of Master of Science in Electronic Engineering - Code: 5468F*

Notes:

- (i) These programme regulations should be read in conjunction with the University's Taught Programme Regulations.
- (ii) All optional modules are offered subject to the constraints of the timetable and to any restrictions on the number of students who may be taught on a particular module. Not all modules may be offered in all years and they are listed subject to availability.
- (iii) A compulsory module is a module which a student is required to study.
- (iv) A core module for outcomes is a module which a student must pass.
- (v) A core module for PSRB accreditation is a module a student is required to obtain accreditation.
- (vi) All modules are delivered in Linear mode unless stated otherwise as Block, eLearning or distance learning.
- (vii) *Degree of Master of Science in Electronic Engineering Code: 5468F, is a non-accredited Masters degree title awarded where a candidate only meets the requirements of the University's Taught Programme Regulations and not the requirements of accreditation.

1. Programme structure

- (a) The programme is available for study in full-time mode only.
- (b) The period of study for full-time mode shall be 1 year starting in September.
- (c) The programme comprises modules to a credit value of 180.

(d) All candidates shall take the following compulsory modules:

Code	Descriptive	Total	Credits	Credits	Credits	Level	Core for	Core for	Mode
	title	Credits	Sem 1	Sem 2	Sem 3		PSRB Accredita	outcomes	
							tion		
EEE8087	Real Time	20	20			7	Yes	Yes	Block
ELLOGO	Embedded	20	20			,	163	163	DIOCK
	Systems								
EEE8088	Reconfigurab	20		20		7	Yes	Yes	Block
	le Hardware	-							
	Design								
EEE8089	M2M	20	20			7	Yes	Yes	Block
	Technology								
	Internet of								
	Things								
EEE8097	Individual	60		10	50	7	Yes	Yes	
	Project								
EEE8119		20		20		7	Yes	Yes	Block
	Communica								
	tion								
	Networks								
	and								
	Security								
EEE8161	Machine	20		20		7			Block
	Learning for								
	Engineering								
	Applications								
EEE8165	Research	20	20			7			Block
	Skills and								
	Developme								
	nt for								
	Engineers								

2. Assessment methods

Details of the assessment pattern for each module are explained in the module outline.

(*) For the purpose of professional accreditation, the University's Education Committee has approved a variation to the Taught Programme Regulations to the effect that a candidate who passes all modules and fails up to 20 credits is recommended, as of right, for the award of an appropriate Master's degree or Postgraduate Diploma, provided that no mark is below 40 and the weighted average mark for all modules and all non-modular aggregated assessment is at least 50.

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