

Programme Regulations: 2025/26

Programme Titles

Degree of Master of Engineering with Honours in Electronics and Computer Engineering with Industrial Project - UCAS Code: H654

Degree of Master of Engineering with Honours in Electronics and Computer Engineering with Industrial Project with Placement Year - Code: 1191U

Degree of Master of Engineering with Honours in Electronic Engineering Science with Industrial Project - Code: 1860U*

Degree of Master of Engineering with Honours in Electronics and Computer Engineering with Industrial Project with International Study Year – Code: 1913U (Year 3)*

Degree of Master of Engineering with Honours in Electronics and Computer Engineering with Industrial Project - Code: 1916U (Year 3)*

Notes

- (i) *These programme regulations should be read in conjunction with the University's Taught Programme Regulations.*
- (ii) *All optional modules are offered subject to the constraints of the timetable and to any restrictions on the number of students who may be taught on a particular module. Not all modules may be offered in all years and they are listed subject to availability.*
- (iii) *A compulsory module is a module which a student is required to study.*
- (iv) *All modules are delivered in Linear mode unless stated otherwise as Block, eLearning or distance learning.*
- (v) *If a candidate meets the requirements for the three-year degree BEng in Electronics and Computer Engineering (H652) they may transfer to that programme at any time before the start of Stage 3.*
- (vi) *Programme transfers for Student Visa students may be restricted. Please refer to the Visa Team for advice.*
- (vii) *Programme coded 1860U is a non-accredited Honours degree title and is awarded where a candidate only meets the requirements of the University's Taught Programme Regulations and Examination Conventions.*
- (viii) **Programme coded 1913U and 1916U are time limited codes for 2024 entry only under special circumstances. These programme are not accredited.*

1. Stage 1

All candidates shall take the following compulsory modules:

Code	Descriptive title	Total Credits	Credits Sem 1	Credits Sem 2	Level
CSC1036	Programming Portfolio	30		30	4
ENG1001	Engineering Mathematics I	20	10	10	4
ENG1003	Electrical and Magnetic Systems	15	10	5	4
ENG1004	Electronics & Sensors	10		10	4
ENG1006	Properties and Behaviour of Engineering Materials	15	15		4
ENG1008	Introduction to Programming Languages (C, Matlab and Python)	15	8	7	4
ENG1009	Sustainable Design, Creativity and Professionalism	15	7	8	4

2. Stage 2

(a) All candidates shall take the following compulsory modules:

Code	Descriptive title	Total Credits	Credits Sem 1	Credits Sem 2	Level
CSC2031	Security Programming	20	20		5
EEE2008	Project and Professional Issues	20		20	5
EEE2009	Signals and Communications	20	20		5
EEE2014	Semiconductor Devices and Analogue Electronics	20	20		5
EEE2019	Engineering Internet of Things Project	10		10	5
EEE2021	Computer programming and Organisation	20	10	10	5
ENG2025	Digital Electronics	10		10	5

(b) To progress to Stage 3 of this degree programme, candidates are required to obtain an average over all modules taken at Stage 2 of at least 55 at the first attempt.

3. Year 3 (Placement Year)

(a) International Study Year - Programme 1913U only (special registration only for 2024)

On completion of Stage 2 and before entering Stage 3, candidates may as part of their studies for the degree spend a year abroad at an appropriate exchange partner institution. Permission to undertake a year abroad is subject to the approval of the Degree Programme Director. Students who are required to re-sit their Stage 2 assessment must delay the start of their placement until they have done so. Students who fail Stage 2 may not complete a year abroad.

Code	Descriptive title	Total Credits	Credits Sem 1	Credits Sem 2	Credits Sem 3	Level	Type	Mode
ISY3000	International Study Year	120	60	60	0	6		

(b) Career Placement Year – Programme 1916 only (special registration only for 2024)

On completion of Stage 3 and before entering Stage 4, candidates may as part of their studies for the degree spend a year in a placement with an approved organisation. Permission to undertake a placement is subject to the approval of the Degree Programme Director. Students who are required to re-sit their Stage 3 assessment must delay the start of their placement until they have done so. Students who fail Stage 3 may not complete a placement year.

Code	Descriptive title	Total Credits	Credits Sem 1	Credits Sem 2	Level
NCL3000	Career Service Placement Year Module	120	60	60	6

4. Stage 3

(a) All candidates shall take the following compulsory module(s):

Code	Descriptive title	Total Credits	Credits Sem 1	Credits Sem 2	Level
CSC3632	System and Network Security	20	20		6
EEE3030	Signal Processing and Machine Learning	20	10	10	6
EEE3094	Individual Project and Technical Report	30	10	20	6
ENG2032	Business and Law for Engineers	10	10		5

EEE3027	Integrated Circuit Design & Embedded Systems	20	10	10	6
---------	--	----	----	----	---

(b) All candidates must take one of the following options:

Code	Descriptive title	Total Credits	Credits Sem 1	Credits Sem 2	Level	Pathway **
EEE3023	Digital Communication Systems	20	10	10	6	Comms
EEE3026	Electrical Devices & Semiconductor Technology	20	10	10	6	Micro

(c) To progress to Stage 4 of this degree programme, candidates are required to obtain an average over all modules taken at Stage 3 of at least 50 at the first attempt.

5. Year 4 (Placement Year)

On completion of Stage 3 and before entering Stage 4, candidates may as part of their studies for the degree spend a year in a placement with an approved organisation. Permission to undertake a placement is subject to the approval of the Degree Programme Director. Students who are required to re-sit their Stage 3 assessment must delay the start of their placement until they have done so. Students who fail Stage 3 may not complete a placement year.

Code	Descriptive title	Total Credits	Credits Sem 1	Credits Sem 2	Level
NCL3000	Career Service Placement Year Module	120	60	60	6

6. Stage 4

(a) All candidates shall take the following compulsory modules:

Code	Descriptive title	Total Credits	Credits Sem 1	Credits Sem 2	Level
EEE8111	Engineering Study Project	10	10		7
EEE8113	Group Design Project	30		30	7
EEE8114	Industrial Project	40	40		7

(b) All candidates shall take the following specialist module.

Code	Descriptive title	Total Credits	Credits Sem 1	Credits Sem 2	Level	Mode
EEE8088	Reconfigurable Hardware Design	20		20	7	Block

(c) All candidates must take one of the following optional modules:

Code	Descriptive title	Total Credits	Credits Sem 1	Credits Sem 2	Level	Pathway **
EEE8119	Communication Networks and Security	20		20	7	Comms
EEE8124	Low Power VLSI Design	20		20	7	Micro

7. Assessment Methods

Details of the assessment pattern in each module are explained in the module outline. To satisfy IET accreditation requirements, a module comprising two assessment modes (coursework and examination) that assess different learning outcomes and each mode contributes more than 30% to the overall module mark,

can only be passed if neither assessment mode is awarded a mark that is no more than 10% below the normal module pass mark.

8. Compensation and Condonement

For students entering the programme*** in 2021/22 onwards, the Engineering Council's policy on compensation and condonement will apply to marks awarded for modules at all stages, to satisfy accreditation requirements. To be awarded an accredited honours degree, only a maximum of 30 credits can be compensated over the duration of the degree programme, where the final mark is up to 5 percentage points below the pass mark****. Core modules cannot be compensated. Individual projects and group projects worth more than 20 credits cannot be compensated.

There is no condonement of modules delivering Accreditation of Higher Education Programmes (AHEP) learning outcomes.

Any student not satisfying the accreditation requirements, but satisfying the University's Degree and Assessment regulations, will have the opportunity to be awarded a non-accredited honours degree with its classification based on the overall final stage averages beyond stage one.

***Note that for Electrical & Electronic Engineering programmes, the above text on compensation and condonement has been applicable for students who started stage one from 2018/19.

****Note that for IET-accredited Electrical and Electronic Engineering degree programmes, a module comprising assessed components worth at least 30% of the overall module mark can only be passed if the overall module mark achieved is at least the pass mark and none of those assessed components have a mark that is more than 10 marks below the pass mark.

9. Degree classification

Candidates will be assessed for degree classification on the basis of all the modules taken at Stages 2 and 3 with the weighting of the stages being 1:3 for Stage 2 and Stage 3 respectively.

Degree of Master of Engineering with Honours in Electronic Engineering Science with Industrial Project - Code: 1860U, is a non-accredited Masters degree title awarded where a candidate only meets the requirements of the University's Taught Programme Regulations and Examination Conventions and not the requirements of accreditation.

**Pathway

Comms – Communications
Micro - Microelectronics